UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: H. Zhang Serial No.: 09/362,808

Art Unit Examiner: 2814

Thanh V.

Filed Title :, July 28, 1999

: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants call attention to the enclosed documents listed on the attached form PTO-1449.

The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language hence no concise explanation is necessary per rule 97(c).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Respectfully submitted,

Date: 0/5/08

Seaftt C. Harris Reg. No. 32,030

SCH/smr

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